



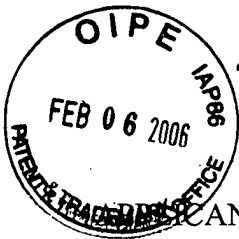
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| TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i> | Application Number | 10/804,974 | |
| | Filing Date | March 18, 2004 | |
| | First Named Inventor | Alzon B. Canilao | |
| | Group Art Unit Number | 2817 | |
| | Examiner Name | Hieu P. Nguyen | |
| Total Number of Pages in This Submission | 5 | Attorney Docket Number | 24970-10887 |

| ENCLOSURES (check all that apply) | |
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| SIGNATURE OF ATTORNEY OR AGENT | | |
|--------------------------------|-----------------------------------|---------------------|
| Signature: | | |
| Attorney/Reg. No.: | Edward B. Weller, Reg. No. 37,468 | Dated: Feb. 2, 2006 |

| CERTIFICATE OF MAILING | | |
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| Express Mail Mailing Number (optional): | | |



PATENT

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

APPLICANT(S): Alzon B. Canilao, Ronald P. Green and Gi Young Lee
APPLICATION NO.: 10/804,974
FILING DATE: March 18, 2004
TITLE: Heterostructure Bipolar Transistor Power Amplifier Module With
Dynamic Voltage Supply For Improved Efficiency
EXAMINER: Hieu P. Nguyen
GROUP ART UNIT: 2817
ATTY. DKT. NO.: 24970-10887

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Dated: Feb. 2, 2006 By: Edward B. Weller
Edward B. Weller, Reg. No.: 37,468

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LETTER TO THE CHIEF DRAFTSPERSON

SIR:

Please substitute the enclosed three (3) sheets of formal drawings containing Figures 1-6 for the three (3) sheets of drawings containing Figures 1-6 previously submitted.

Respectfully submitted,
Alzon B. Canilao, Ronald P. Green and Gi Young Lee

Dated: Feb. 2, 2002 By: Edward B. Weller
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